

## **AMENDMENTS TO THE TITLE AND SPECIFICATION**

### **IN THE TITLE:**

Please amend the title as follows:

METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE HAVING A PLURALITY OF WIRING LAYERS AND—ITS  
~~MANUFACTURING METHOD, AND MASK-PATTERN GENERATION METHOD~~

### **IN THE SPECIFICATION:**

On page 1, before the heading “TECHNICAL FILED,” please insert the following heading and paragraph:

#### **“CROSS-REFERENCE TO RELATED APPLICATIONS”**

This application is a Divisional application of Application Serial No. 09/959,576, filed October 30, 2001, which was filed from a PCT application number PCT/JP99/02315, filed April 30, 1999, the entire disclosure of which is hereby incorporated by reference.